# **Electronic Acknowledgement Receipt** FES ID: 1178689 Application Number: 10696615 Confirmation Number: 6846 Semiconductor memory devices having offset transistors and methods Title of Invention: of fabricating the same First Named Inventor: Se-Jin Ahn Customer Number: 20792 Filer: D. Randal Ayers/Michele McMahan Filer Authorized By: D. Randal Ayers Attorney Docket Number: 5649-1202 Receipt Date: 30-AUG-2006 Filing Date: 28-OCT-2003 Time Stamp: 15:38:39 Application Type: Utility International Application Number:

## Payment information:

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### File Listina:

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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		Response_5649_1202.pdf	814550	yes	28

	Doc Desc	Start	End		
	Amendment - After Non-Final Rejection	1	1		
	Applicant Arguments/Remarks Made in an Amendment	2	6		
	Translation of Foreign Priority Documents	7	28		
Warnings:		•			
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#### New Applications Under 35 U.S.C. 111

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

#### National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.